

#3 | 1DS  
3-21-01  
R. States

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): SUZUKI et al.  
Serial No.: Unknown  
Filed: Concurrently herewith  
Title: SEMICONDUCTOR DEVICE AND  
METHOD FOR MANUFACTURING  
THE SAME

Atty. Dkt.: 1-85

Group Art Unit:

Examiner:



Assistant Commissioner for Patents  
Washington, D.C. 20231

Date: September 29, 2000

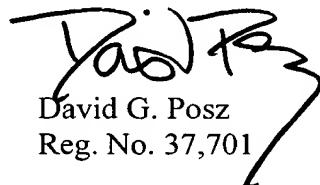
INFORMATION DISCLOSURE STATEMENT

Sir:

Pursuant to 37 C.F.R. §1.56, the references listed on the attached Form PTO-1449 are being submitted for consideration by the Examiner without any admission that they constitute statutory prior art, or without any admission that they contain subject matter that anticipates the invention or renders the invention obvious to a person of ordinary skill in the art.

The Examiner is requested to initial the attached PTO Form-1449 and to return a copy of same to the undersigned attorney as proof that the listed references have been considered and made of record.

Respectfully submitted,

  
David G. Posz  
Reg. No. 37,701

Law Office of David G. Posz  
601 Pennsylvania Avenue, N.W.  
Suite 900, South Building  
Washington, D.C. 20004  
(202) 220-3105 (phone)  
Customer No. 23400